Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination GAZZOLA ET AL. | Examiner | Art Unit | Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-2003/0016423	01-2003	Cho et al.	359/189
*	В	US-2002/0126784	09-2002	Brazeau et al.	375/371
*	C	US-6,466,886	10-2002	Marmur, Oren	702/126
*	D	US-6,178,213	01-2001	McCormack et al.	375/355
*	E	US-7,050,463	05-2006	Cho et al.	370/476
*	F	US-6,684,033	01-2004	Doh et al.	398/202
*	G	US-5,982,837	11-1999	Earnest, Tim	375/377
	Н	US-			
	-	US-			
	J	·US-			
	·K	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	Z					
	0	,				
	Р					
	α					·
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Scheytt et al: "A 0.155-, 0.622-, and 2.488-Gb/s Automatic Bit-Rate Selecting Clock and Data Rrecovery IC for Bit-Rate Transparent SDH Systems", IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, page 1935-1943
	v	
	w	
	x	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

. . . .